

2) AS MUCH AS I ENJOY DOING THE NEWSLETTER, IT ABSORBS AN ENORMOUS AMOUNT OF TIME. THUS IT SEEMS NECESSARY TO SET UP A WAY OF SHARING THE EDITORSHIP WITH OTHER PARTICIPANTS. THE CABRILLO COMPUTER CENTER CAN STILL SERVE AS A CLEARING HOUSE FOR INCOMING MAIL AND A DISTRIBUTION POINT FOR OUTGOING NL'S. INFORMATION SENT IN BY PARTICIPANTS CAN BE FORWARDED TO THE CURRENT EDITOR FOR THE UPCOMING ISSUE.

3) IT MAY BE DESIRABLE TO HAVE TWO RELATIVELY INDEPENDENT NEWSLETTERS OR AT LEAST SEPERATE EDITORS FOR A HARDWARE SECTION AND A SOFTWARE SECTION. STEVE DIAMOND, 311 CARL ST., SAN FRANCISCO, CA 94117 HAS EXPRESSED AN INTEREST IN ORGANIZING THE SOFTWARE END.

PLEASE JOT DOWN YOUR DESIRES AND SUGGESTIONS AS TO WHAT YOU WANT TO SEE HAPPEN IN THE FUTURE. I'LL TRY TO SUMMARIZE THEM IN NL #5 AND PUBLISH A DECISION AS TO WHAT WILL HAPPEN IN THE FUTURE. AS ONE READER POINTED OUT, "THIS THING IS THE TIP OF AN ICEBURG." I WONDER WHAT'S REALLY UNDERNEATH?

BUGS

ROBERT COOK PROVIDED US WITH AN UP-TO-DATE LIST OF BUGS. HIS MARK-8 IS RUNNING SO IT SHOULD BE ACCURATE AND COMPLETE. THERE ARE A FEW OTHER THINGS MENTIONED BY OTHER USERS THAT HAVE BEEN ADDED.

1. P. 6, PARAGRAPH 4, LAST LINE: CHANGE TO "RESISTORS R1-R4 AND R21."
2. CONNECTIONS ON THE BOTTOM OF THE BOARDS ARE IN PARALLEL. ARRANGE THE BOARDS IN ORDER SHOWN IN THE BOOK AND WIRE, THEN CUT OUT WIRES 9 THRU 16 BETWEEN INPUT MULTIPLEXER AND ADDR/MANUAL BOARDS.
3. P. 4, INPUT MULTIPLEXER PARTS LIST SHOULD READ: "C1, C3, C4 - 0.1 MF DISK, C2 - 100 MF 10 V. ELECTROLYTIC."
4. THE 8 HOLES AT THE TOP OF THE MEMORY BOARD WHERE THE OUTPUT LEADS CROSS THE GROUND LEAD SHOULD NOT!!! BE CONNECTED THRU THE BOARD.
5. INSTALL A JUMPER ON INPUT MULTIPLEXER BOARD BETWEEN IC3 PIN 14 AND +5 VOLTS.
6. ON LED BOARD, C2 GOES FROM B+ TO B+ AND SHOULD GO FROM B+ TO GROUND.
7. ON ADDRESS LATCH BOARD THERE SHOULD BE A JUMPER FROM IC1, PIN 5, TO IC2 PIN 1.
8. IC3 ON CPU BOARD HAS NO GROUND. CONNECT INSULATED WIRE FROM PIN 4 TO GROUND RAIL AND CHECK FOR CHANGE OF STATE OUTPUT ON PINS 10 AND 11. (THIS MAY APPLY ONLY TO SOME BOARD SETS.)

NOTE:

IN CONNECTING THE 41 WIRE BUS FROM BOARD TO BOARD, NOTICE THAT THE TWO SETS OF HOLES ARE NOT ALL CONNECTED AND THOSE THAT ARE CONNECTED ARE ONLY ON ONE SIDE. CHECK EACH CONNECTION TO BE SURE YOU HAVE A COMPLETE CIRCUIT.

ALL OF THE OPERATIONS INVOLVED IN MANUALLY LOADING AND READING MEMORY ARE INDEPENDENT OF THE 8008. IN ORDER TO MINIMIZE DANGER OF DAMAGE TO THE 8008, CHECK OUT ALL OF THIS CIRCUITRY BEFORE INSERTING THE 8008.

YOU MAY HAVE TO DECOUPLE THE CRYSTAL CLOCK, OTHERWISE IT MAY RUN AT 10 MHZ. R3 MAY BE TOO SMALL. (PLESKAC)

THOUGH NOT AN ERROR, IT MIGHT BE WELL TO REMIND PEOPLE TO INSTALL FRONT-TO-BACK JUMPERS ON THE MEMORY BOARD WHEN NOT USING THE "B" JUMPERS 1ST BOARD ONLY. (WILLIAM SEVERENCE)

A PICTURE IN THE MANUAL SHOWS TRIMPOTS. THIS MUST BE THE FIRST PROTOTYPE THAT USED A MULTIVIBRATOR CLOCK INSTEAD OF THE CRYSTAL CLOCK.